

Docket No.: W&B-INF-1852

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By:  Date: September 3, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Michael Sommer  
Appl. No. : 10/626,957  
Filed : July 25, 2003  
Title : Semiconductor Circuit Structure and Method for Fabricating the  
Semiconductor Circuit Structure  
Art Unit :

### LETTER

Hon. Commissioner for Patents,  
Alexandria, VA 22313-1450

Sir:

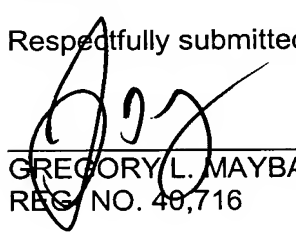
The above-mentioned new patent application was filed on July 25, 2003 without a signed oath or declaration, under the provision of 37 C.F.R. 1.53(f).

In accordance with the above-mentioned rule, enclosed herewith is the original signed declaration.

The undersigned hereby states that the application filed in the Patent and Trademark Office is the application which the inventor(s) executed by signing the declaration. MPEP 602 (8<sup>th</sup> ed., Aug. 2001).

The fee required for the late filing of an oath or declaration in the amount of \$130.00 is also enclosed.

Respectfully submitted,

  
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GREGORY L. MAYBACK  
REG. NO. 40,716

/mjb

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